

RIGOL PA1011 Power Amplifier Module Fact Sheet



Specifications:

1. All are warranted specifications besides those with “typical” indication.
2. Specifications are valid after a 30-minute warm-up period and specified at 25 °C .

Signal Input	
Input Impedance	50KOhm
Built-in Voltage Bias (Output equivalence)	+/-12V
External Input	+/-10Vmax(Gain: X1) +/-1.25Vmax(Gain: X10)
Amplifier	
Gain	10V/1V, 10V/10V Two Ranges switching (DC Gain Error: <5%)
Polarity Switching	Non invert/Invert
P_{RMS} (RL=7.5Ω)	10W (typical, input Sine, 100kHz, X10)
Output Voltage	12.5 V _{Peak} (input Sine, 100kHz)
Output Current	1.65 A _{Peak} (input Sine, 100kHz)
Output Impedance	< 2 Ohm
Full Power Bandwidth (Rated Load)	DC~1MHz *
Output Slew rate	≥80V/μs , typical
Overshoot	<7%
Voltage Bias	
Bias Voltage Gain Error	5%±100mV
Others	
Power	DC 12V±5%, 4A peak
Output Protection	Over-current protection, internal overheat protection
Operating Temperature	0 °C ~+35 °C
Dimensions (W×H×D)	142.2mm×48.1mm×215.4mm
Weight	850g±20g
Others	Portable, seamless connection with RIGOL DG series (need to be upgraded), also could be controlled by PC, output switch control, state indication(include power supply, protection and USB connection)

*Full Power Bandwidth= Slew Rate / 2πVmax, Vmax is the Maximum non-distortion output amplitude